

Prior art

Fig 1

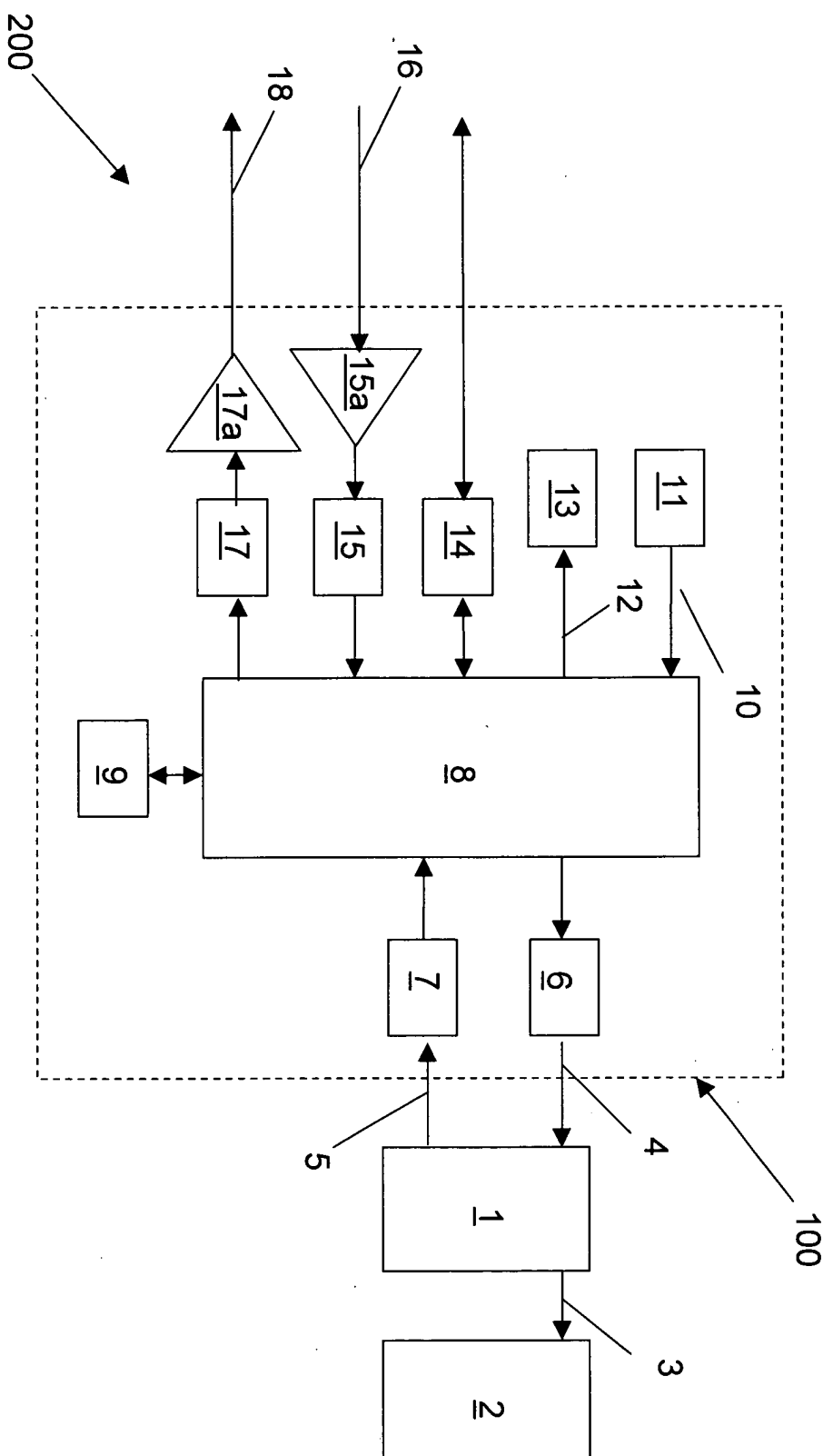
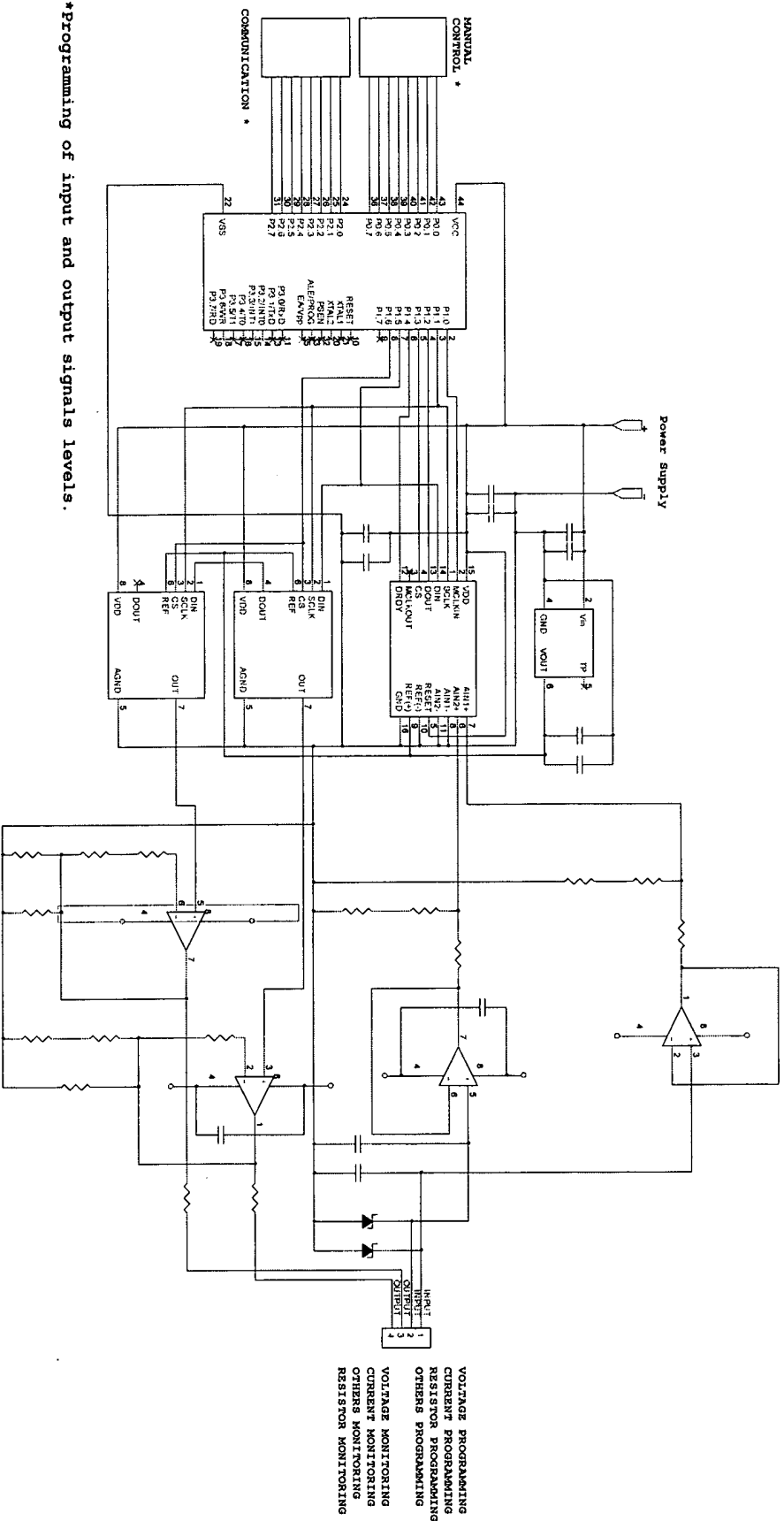


Fig 2

Fig 3



*Programming of input and output signals levels.